

CSM-SP10TG-M85-M3

10.3 Gbps SFP+ Transceiver, Multimode, 300 m Reach

Description

The SFP+ transceivers are high-performance, cost-effective modules supporting data rates up to 10.3 Gbps.

These modules consist of three sections: a VCSEL laser transmitter, a PIN photodiode integrated with a transimpedance preamplifier (TIA), and an MCU control unit.

All modules satisfy Class I laser safety requirements and comply with the SFP+ Multi-Source Agreement (MSA) and the SFF-8472 digital diagnostics interface.

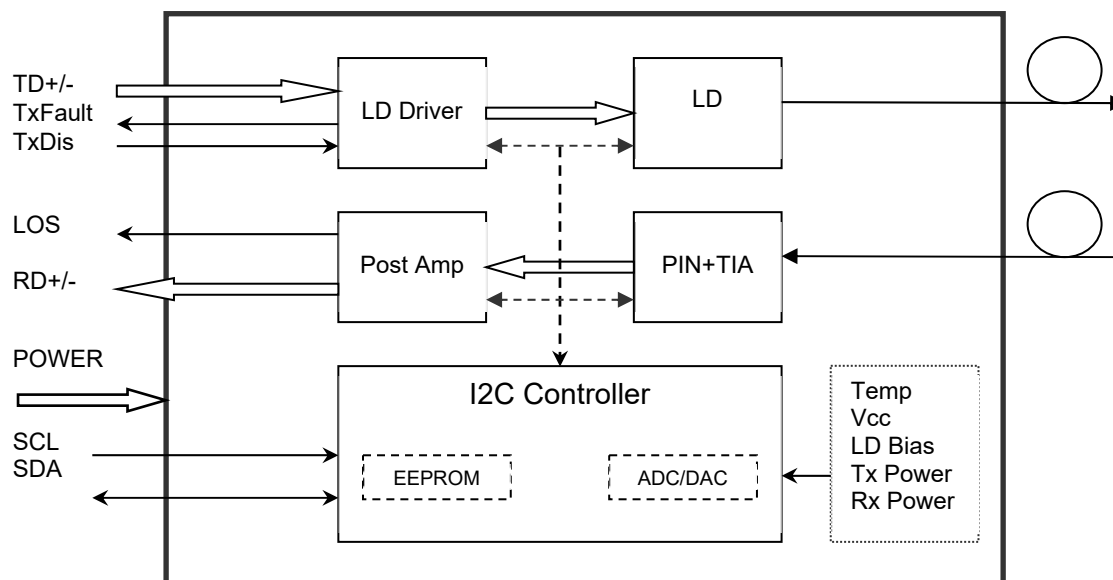
Features

- Supports data rates up to 10.7 Gbps
- Hot-pluggable SFP+ footprint
- 850 nm VCSEL laser and PIN photodiode, supporting transmission distances of up to 300 m over OM3 MMF
- Compliant with SFP+ MSA and SFF-8472, with a duplex LC receptacle
- RoHS compliant
- Single +3.3 V power supply
- Real-time Digital Diagnostic Monitoring
- Operating case temperature:
 - Standard: 0 °C to +70 °C
 - Industrial: -40 °C to +85 °C



Applications

- 10GBASE-SR at 10.3125 Gbps
- 10GBASE-SW at 9.953 Gbps
- Other optical links



Transceiver Functional Block Diagram

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	V _{cc}	-0.5	4	V
Storage Temperature	T _s	-40	+85	°C
Operating Relative Humidity, non-condensing	RH	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	0		+70	°C
	Extended	-20		+80	°C
	Industrial	-40		+85	°C
Power Supply Voltage	V _{cc}	3.135	3.30	3.465	V
Power Supply Current	I _{cc}			260	mA
Data Rate		1.0	10.3	10.7	Gbps

Optical and Electrical Specifications

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Center Wavelength		λ_c	840	850	860	nm	
Spectral Width (RMS)		$\Delta\lambda$			0.45	nm	
Side-Mode Suppression Ratio		SMSR	-	-	-	dB	
Average Output Power		P _{OUT}	-6.0		-0.5	dBm	1
Extinction Ratio		ER	3.0			dB	
Differential Data Input Swing		V _{IN}	180		950	mV	2
Input Differential Impedance		Z _{IN}	90	100	110	Ohm	
TX Disable Input	Disable		2.0		V _{CC}	V	
	Enable		0		0.8	V	
TX Fault Output	Fault		2.0		V _{CC}	V	
	Normal		0		0.8	V	
Receiver							
Center Wavelength		λ_c	840	850	860	nm	
Receiver Sensitivity					-10.5	dBm	3
Receiver Overload			0.5			dBm	3
LOS De-Assert		LOS _D			-12	dBm	
LOS Assert		LOS _A	-22			dBm	
LOS Hysteresis			0.5		4	dB	
Differential Data Output Swing		V _{OUT}	500	700	900	mV	4
LOS Output	High		2.0		V _{CC}	V	
	Low		0		0.8	V	

Notes:

1. The optical power is launched into MMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 2³¹-1 test pattern at 10.3125 Gbps, BER ≤1×10⁻¹².
4. Internally AC-coupled.

Timing Specifications

Parameter	Symbol	Min	Typical	Max	Unit
TX Disable Negate Time	t _{on}			1	ms
TX Disable Assert Time	t _{off}			10	μs
Time to Initialize, including Reset of Tx Fault	t _{init}			300	ms
TX Fault Assert Time	t _{fault}			100	μs
TX Disable To Reset	t _{reset}	10			μs
LOS Assert Time	t _{loss_on}			100	μs
LOS De-assert Time	t _{loss_off}			100	μs
Serial ID Clock Rate	f _{serial_clock}		100	400	kHz
MOD_DEF (0:2) High	V _H	2		V _{cc}	V
MOD_DEF (0:2) Low	V _L			0.8	V

Digital Diagnostic Monitoring Specifications

Parameter	Range	Accuracy	Unit	Calibration
Temperature	0 °C to +70 °C	±3	°C	Internal
	-20 °C to +80 °C			
	-40 °C to +85 °C			
Voltage	3.0 to 3.6	±3 %	V	Internal
Bias Current	0 to 15	±10 %	mA	Internal
TX Optical Power	-6.0 to -0.5	±3 dB	dBm	Internal
RX Optical Power	-16 to -1	±3 dB	dBm	Internal

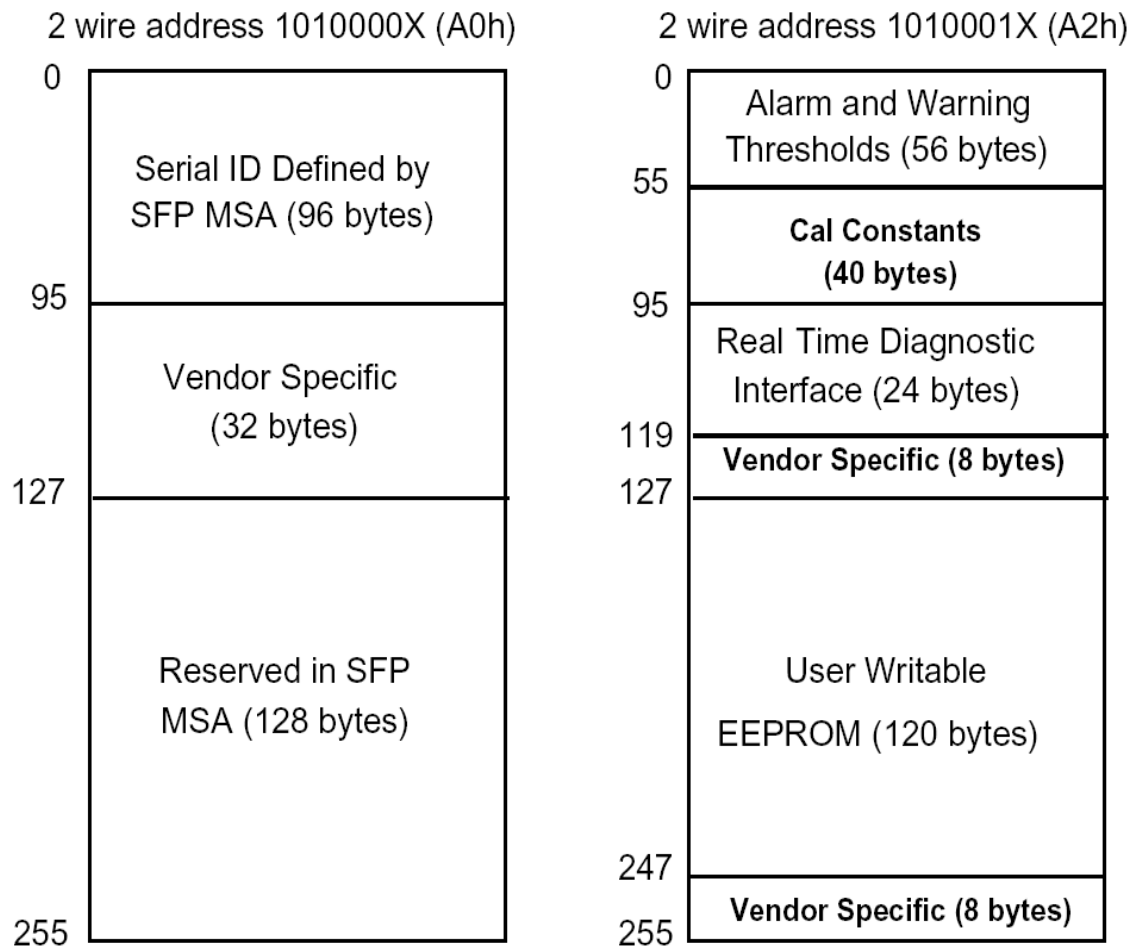
Digital Diagnostic Memory Map

Both internal and external calibration are supported for the diagnostic monitoring parameters, including received power, transmitted power, bias current, supply voltage, and temperature.

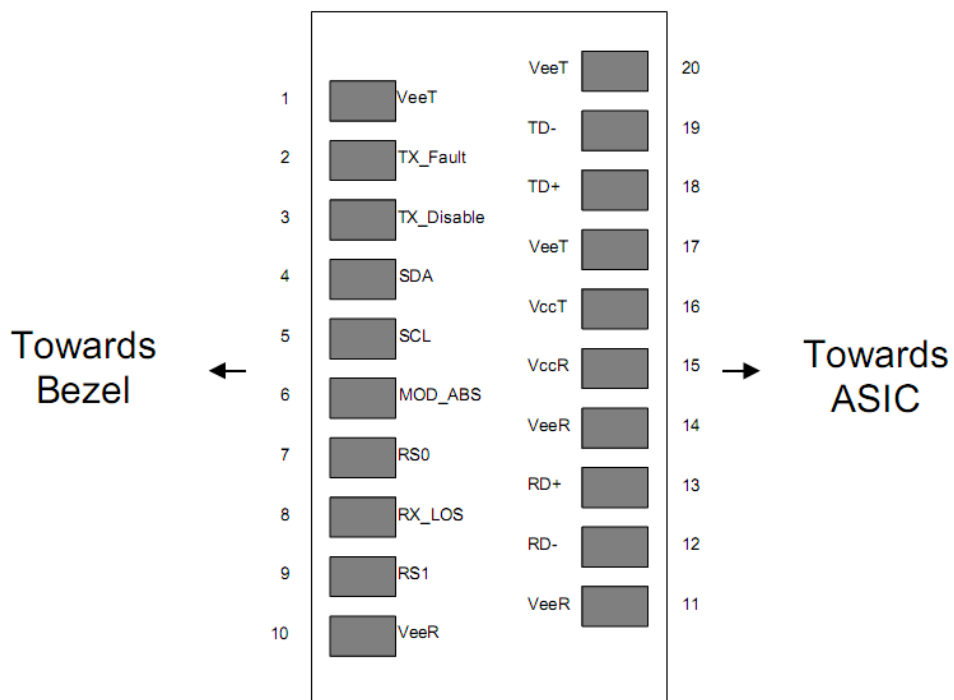
The detailed data field descriptions for the digital diagnostic memory map are outlined below.

The transceivers provide serial ID memory contents and diagnostic information about current operating conditions via the 2-wire serial interface (SCL, SDA).

The diagnostic functions are implemented with internal calibration to monitor received optical power, transmitted optical power, bias current, supply voltage, and temperature. The specific data fields of the digital diagnostic memory map are defined as follows.



Pin Diagram



Pin Descriptions

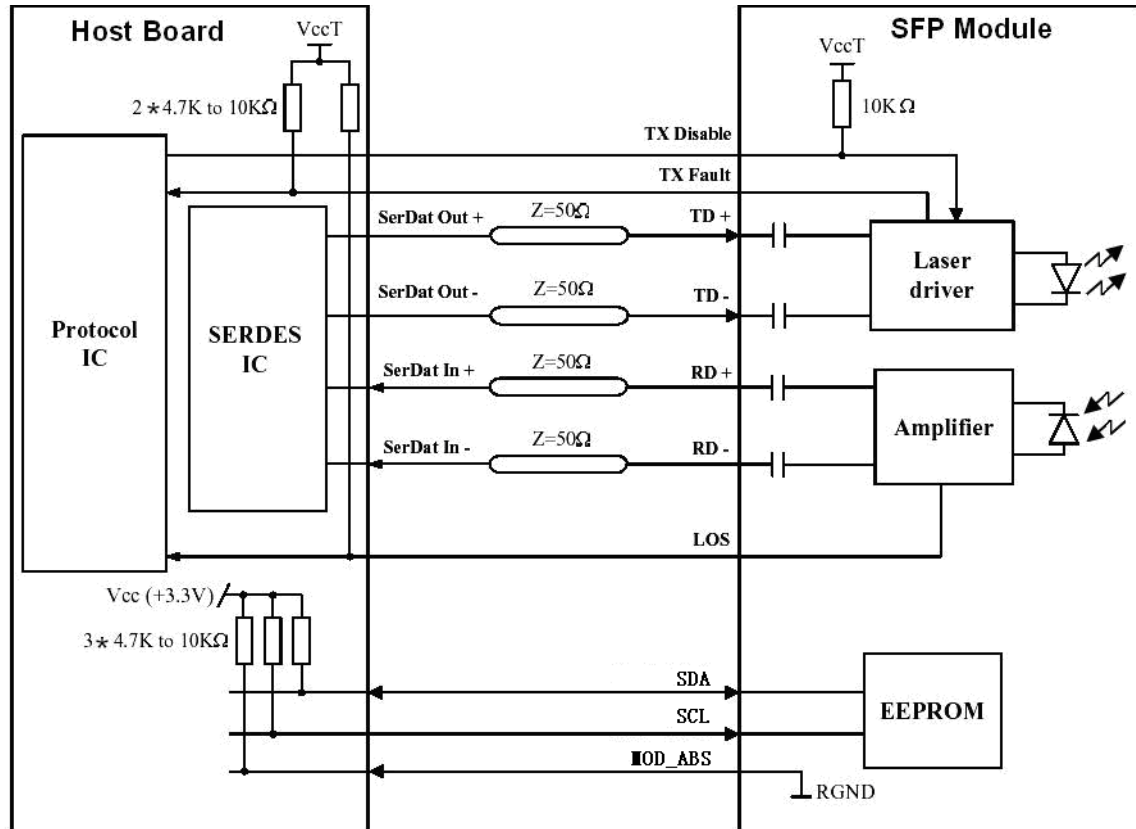
Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	1
3	TX Disable	Transmitter Disable	3	2
4	SDA	Serial Data	3	
5	SCL	Serial Clock	3	
6	MOD_ABS	Module Absent; grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	3
9	RS1	Not Connected	3	
10	V _{EER}	Receiver Ground	1	
11	V _{EER}	Receiver Ground	1	
12	RD-	Inverted Received Data Output	3	4
13	RD+	Received Data Output	3	4
14	V _{EER}	Receiver Ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data Input	3	5
19	TD-	Inverted Transmit Data Input	3	5
20	V _{EET}	Transmitter Ground	1	

Notes:

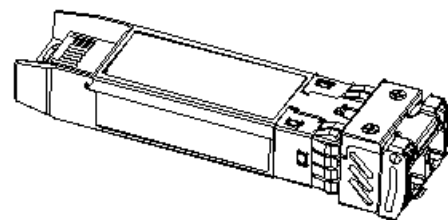
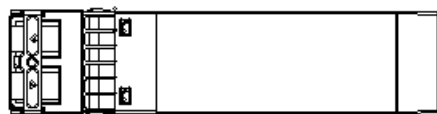
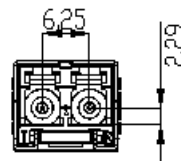
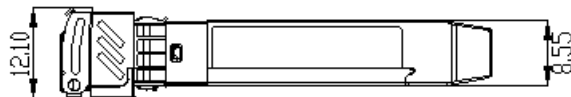
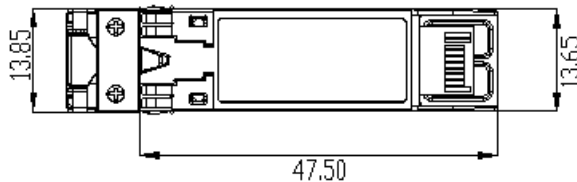
Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open-collector output that should be pulled up with a 4.7 kOhm to 10 kOhm resistor on the host board to a voltage between 2.0 V and V_{cc} + 0.3 V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault. In the low state, the output will be pulled to less than 0.8 V.
- The laser output is disabled when TX Disable is > 2.0 V or left open, and enabled when TX Disable is < 0.8 V.
- LOS is an open-collector output that should be pulled up with a 4.7 kOhm to 10 kOhm resistor on the host board to a voltage between 2.0 V and 3.6 V. Logic 0 indicates normal operation, while Logic 1 indicates loss of signal.
- RD-/RD+: These are the differential receiver outputs. They are internally AC-coupled 100 Ohm differential lines that must be terminated with a 100 Ohm differential load at the host SERDES.
- TD-/TD+: These are the differential transmitter inputs. They are internally AC-coupled 100 Ohm differential lines with internal differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



Unit: mm

Ordering Information

Part Number	Product Description
CSM-SP10TG-M85-M3C	850 nm, 10.3 Gbps, LC, 300 m, 0 °C to +70 °C, with DDM
CSM-SP10TG-M85-M3I	850 nm, 10.3 Gbps, LC, 300 m, -40 °C to +85 °C, with DDM

For More Information

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